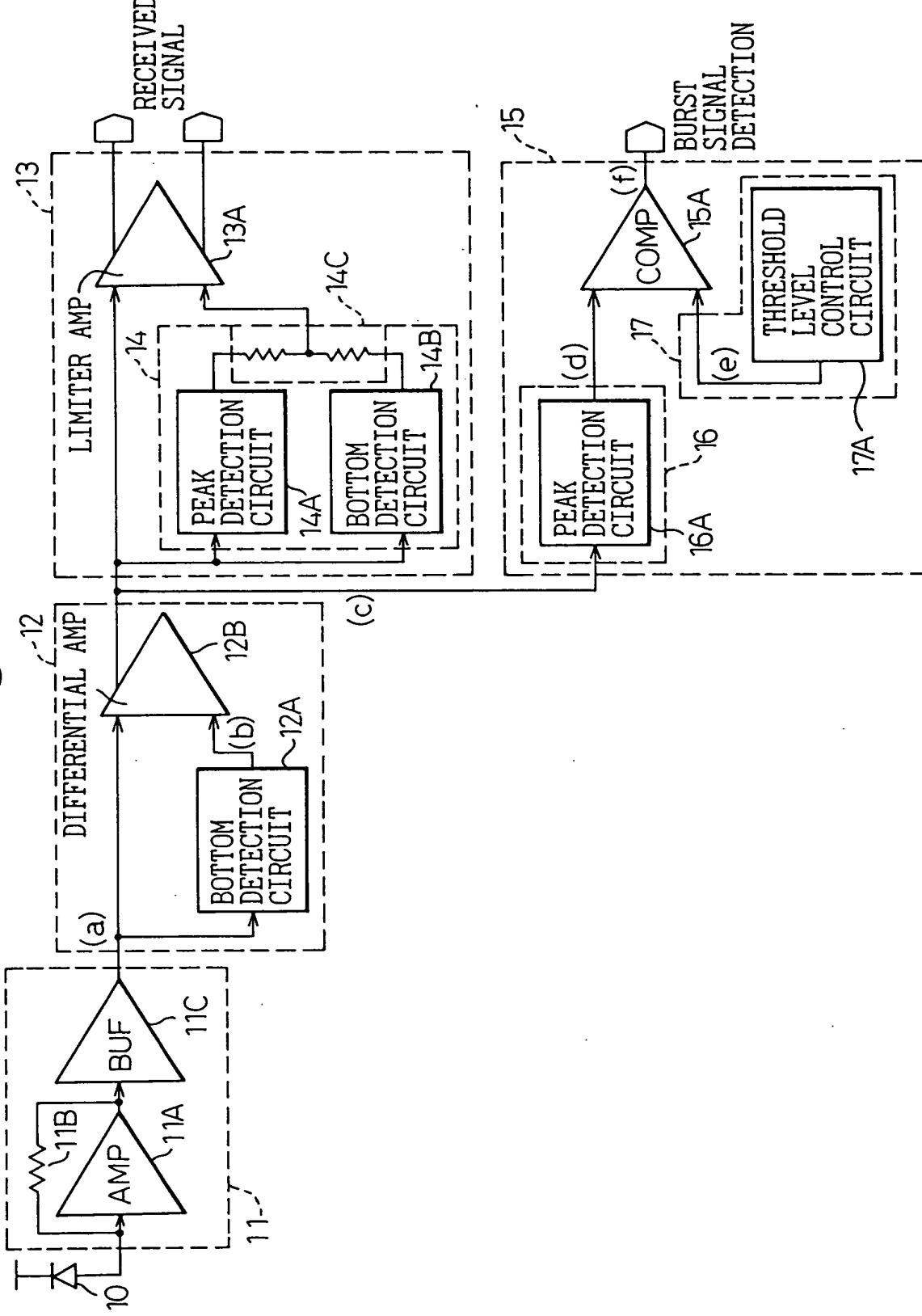


Fig.1



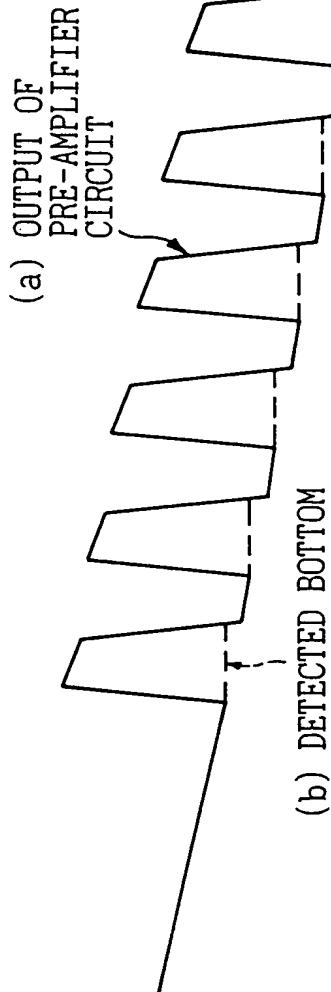


Fig. 2A WAVEFORM IN
CIRCUIT 12

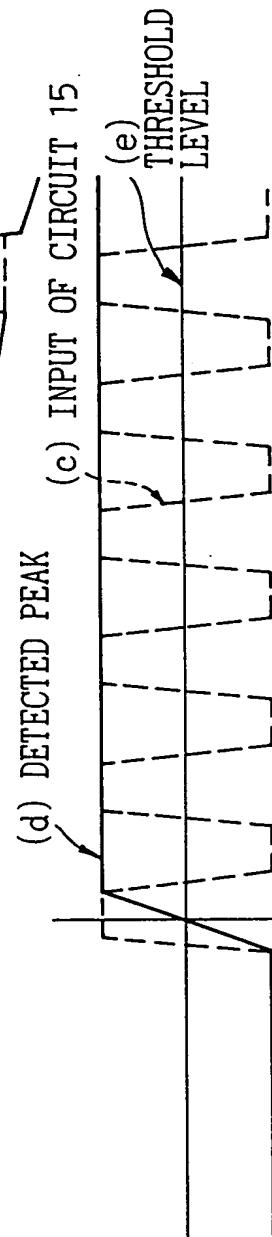


Fig. 2B WAVEFORM IN
CIRCUIT 15

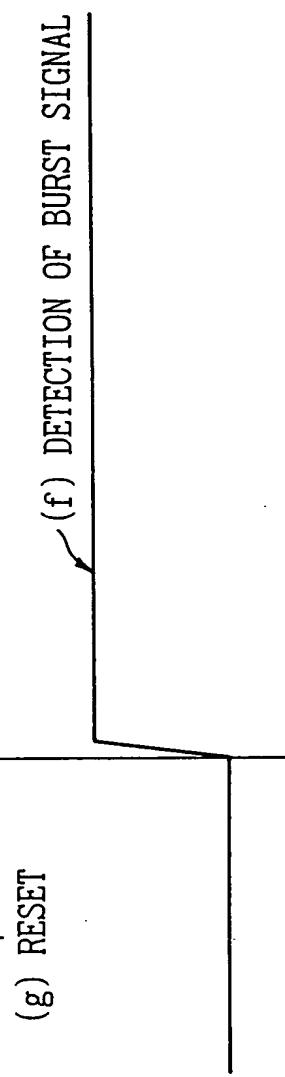


Fig. 2C OUTPUT OF
COMP 15A

Fig.3A

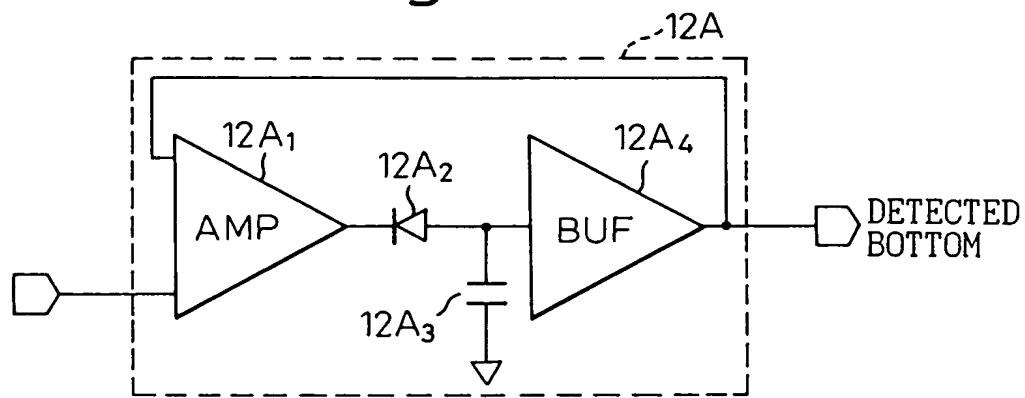


Fig.3B

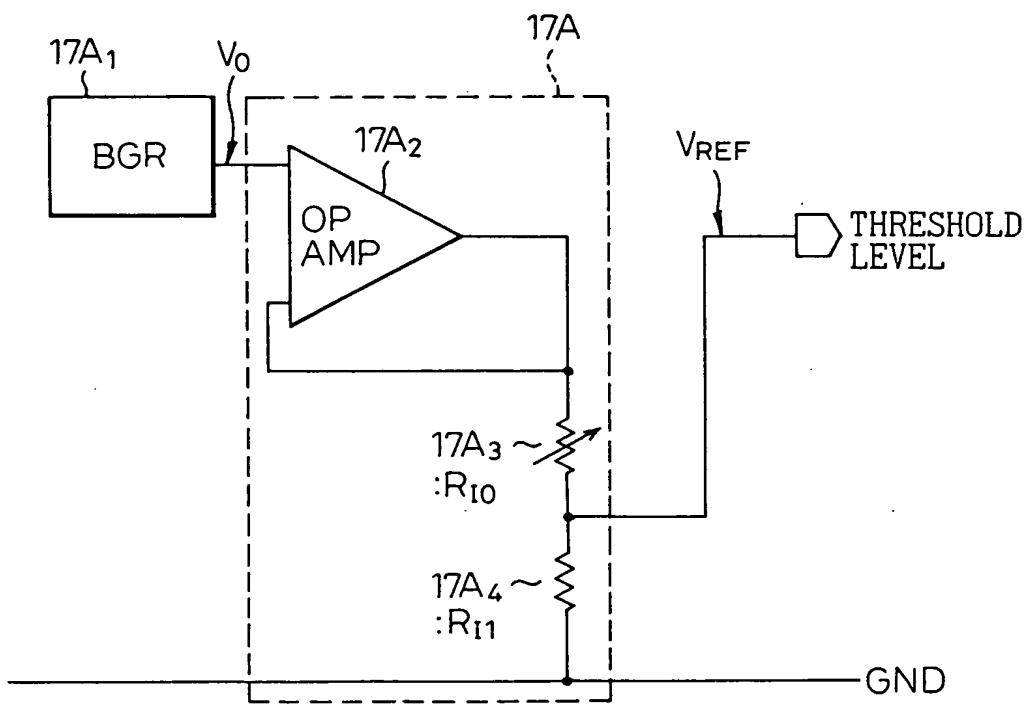


Fig. 4

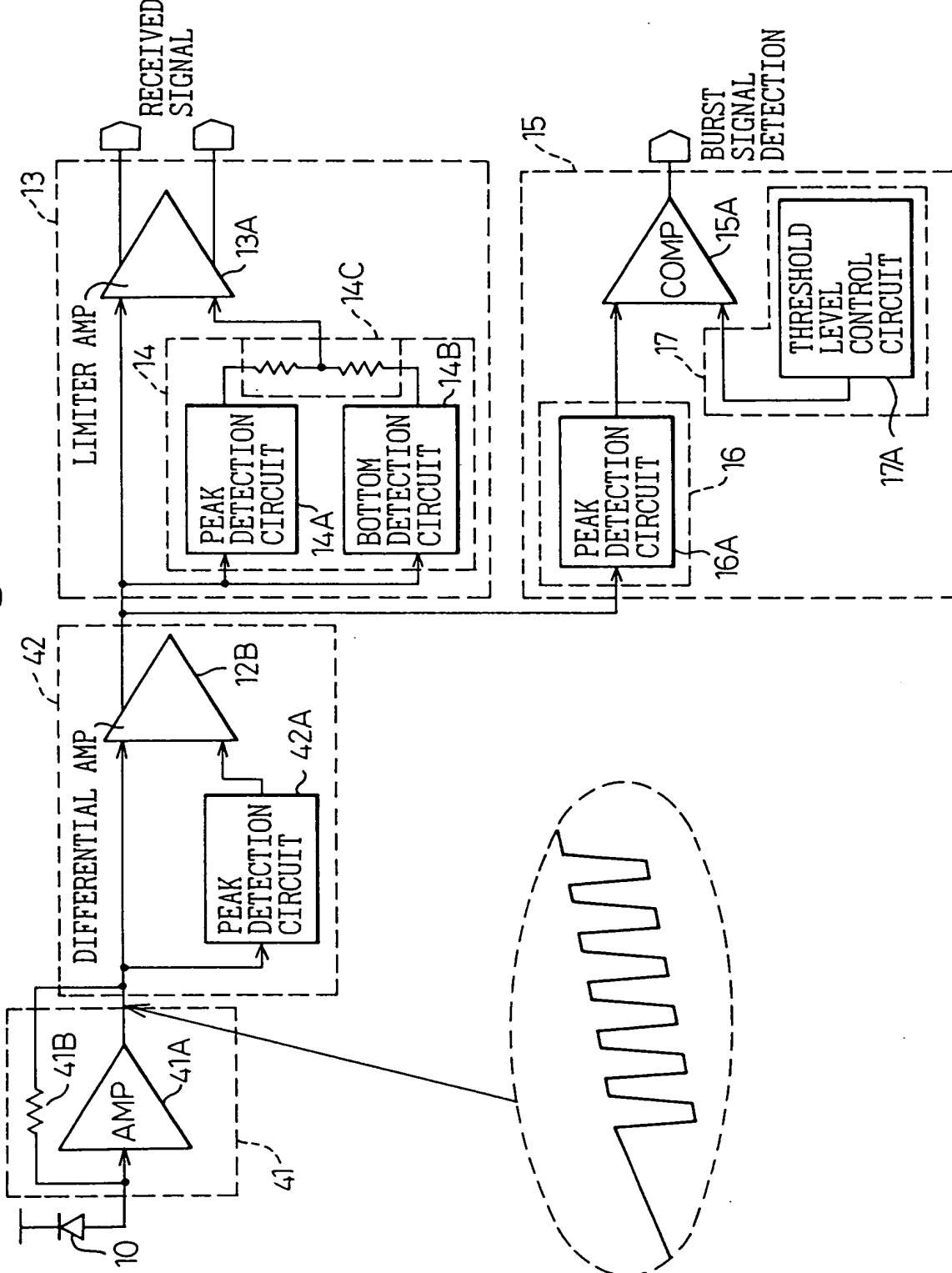
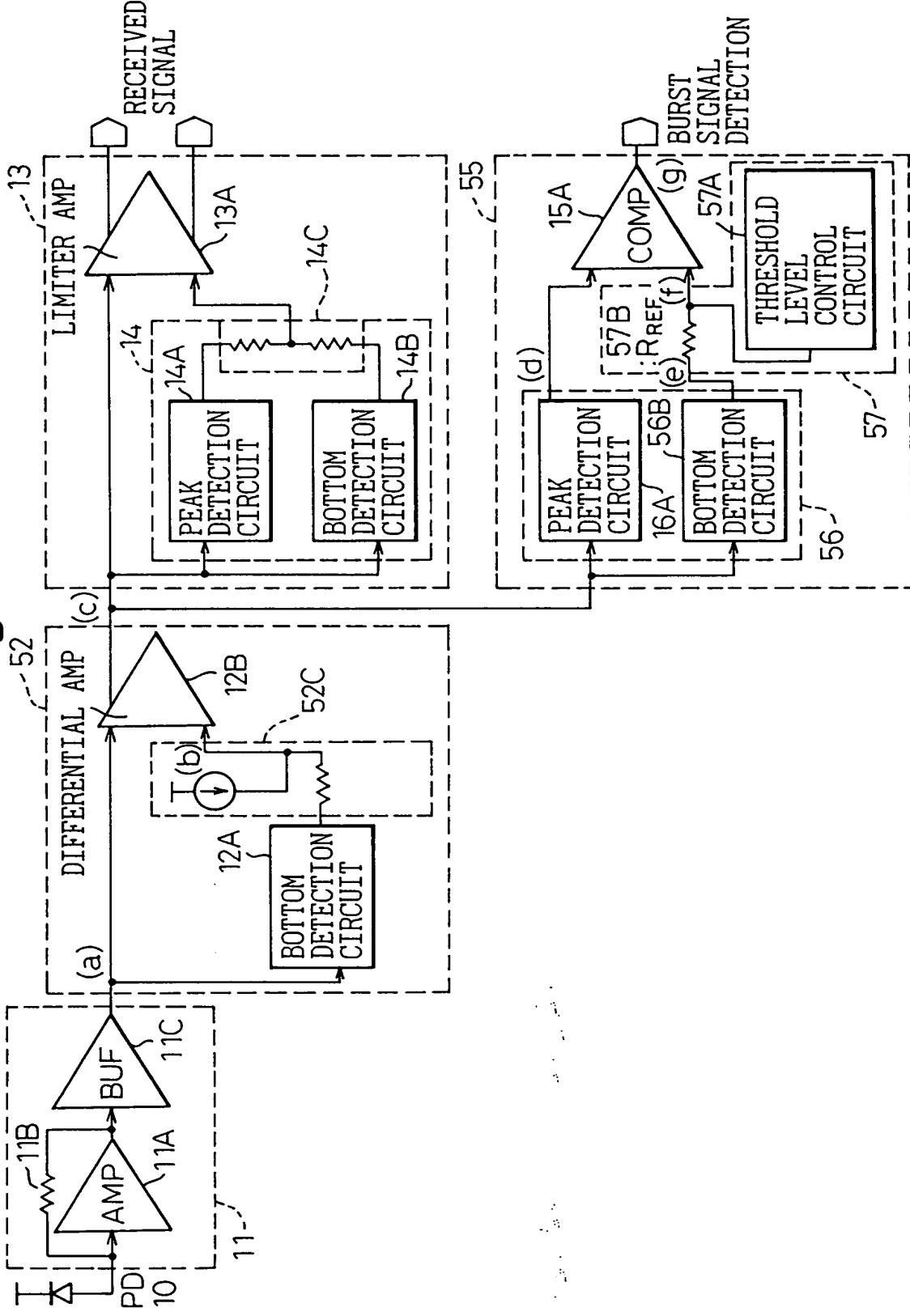


Fig. 5



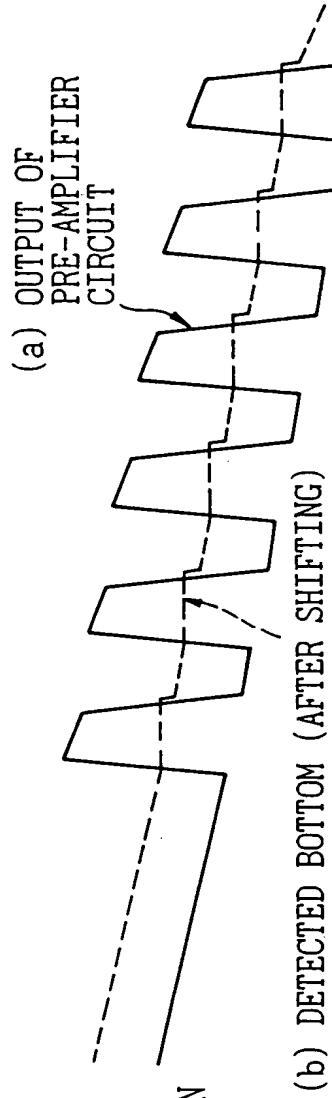


Fig. 6A WAVEFORM IN CIRCUIT 52

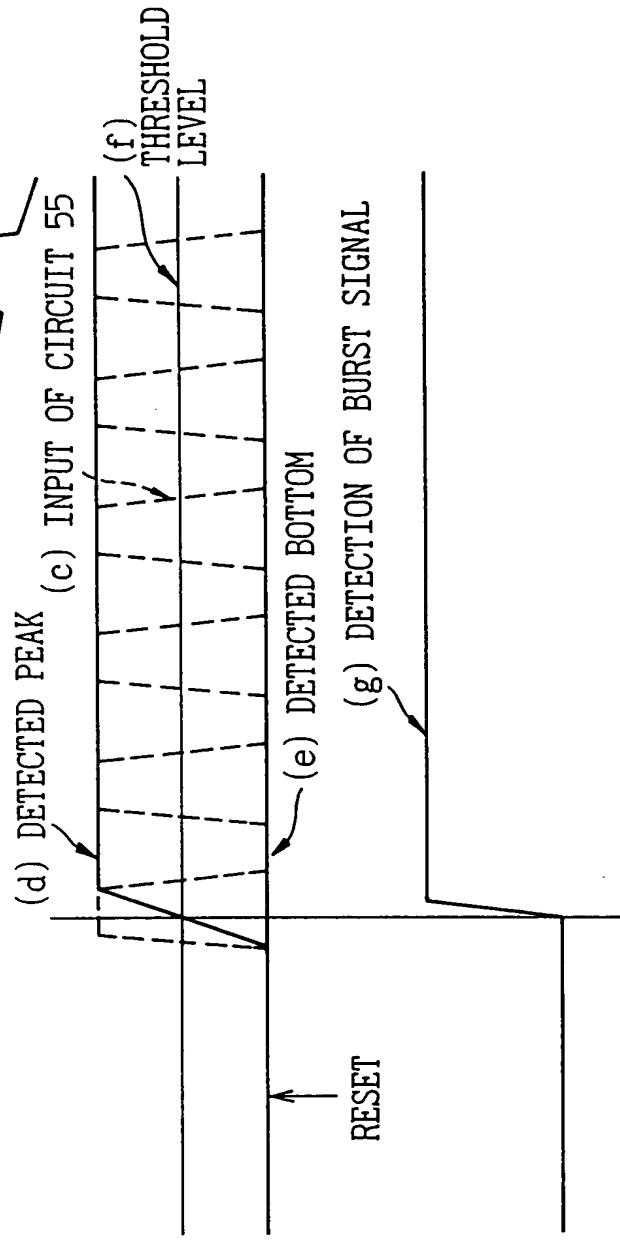


Fig. 6B WAVEFORM IN CIRCUIT 55

Fig. 6C OUTPUT OF COMP 15A

Fig.7

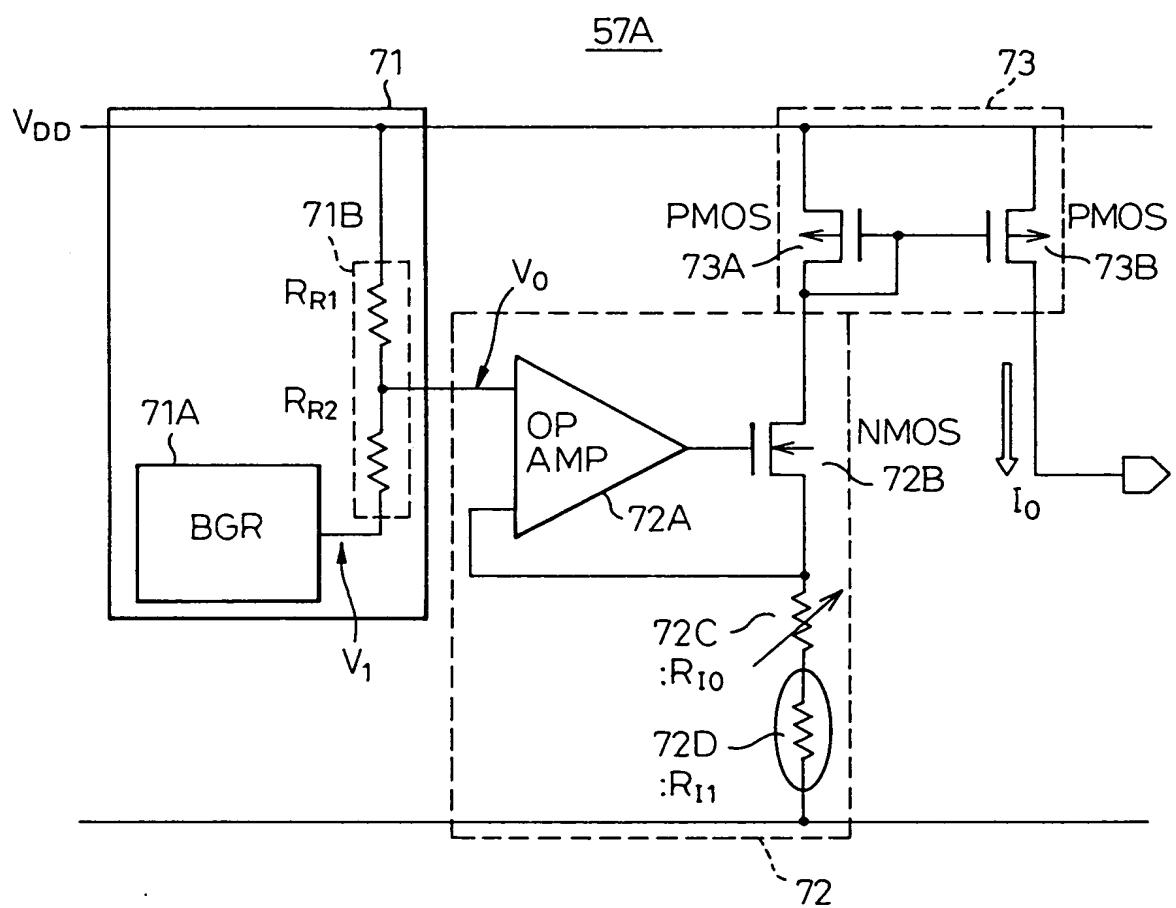
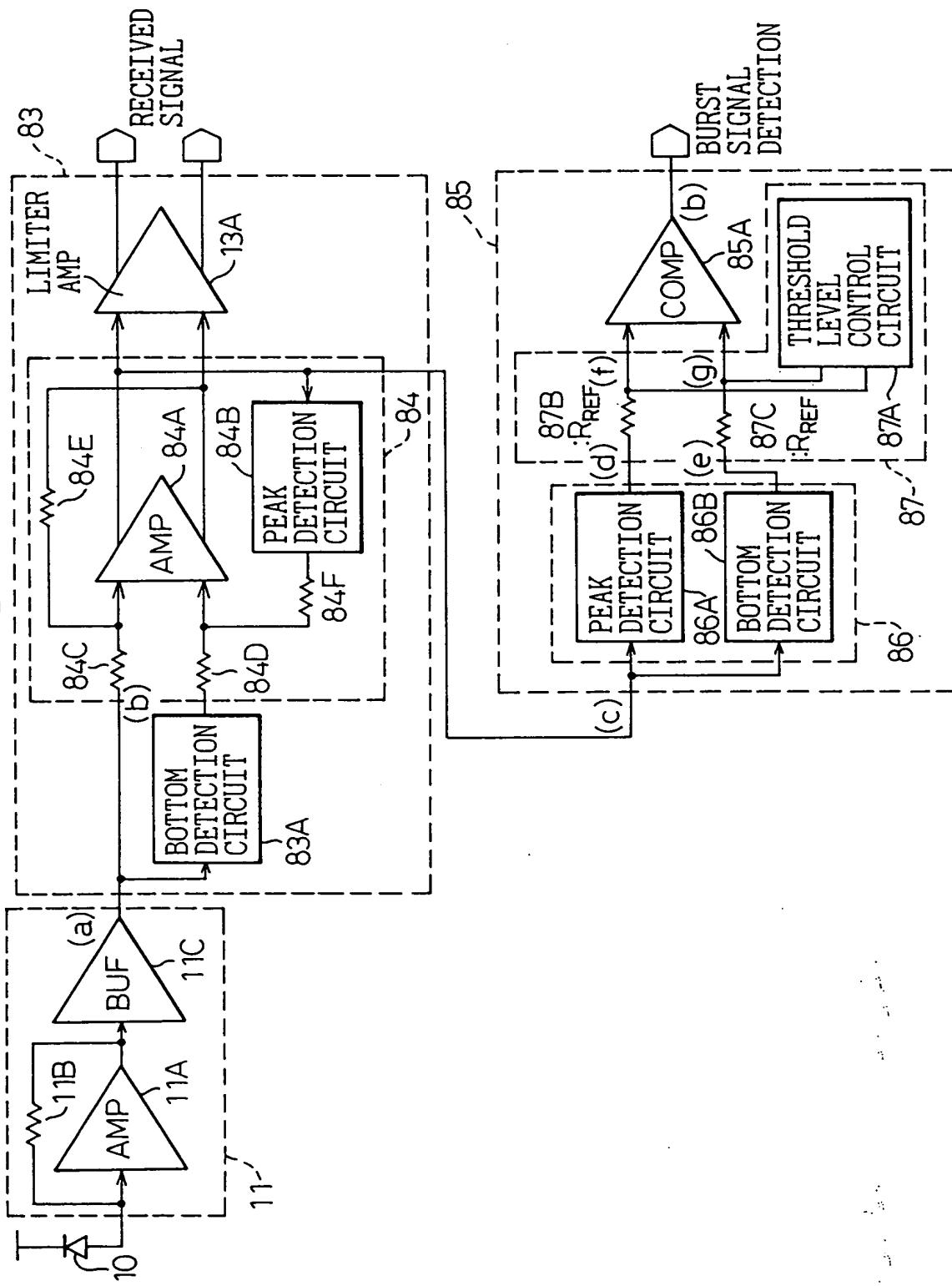


Fig.8



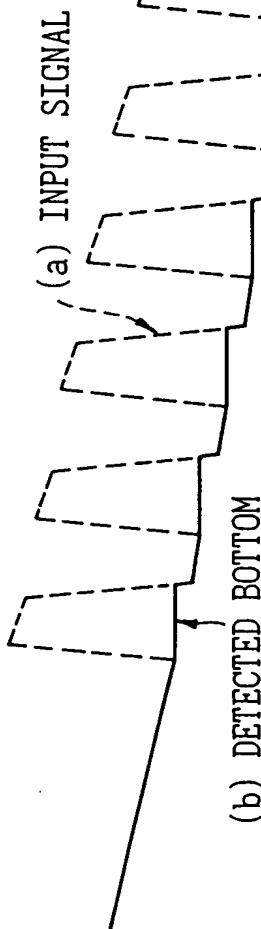


Fig. 9A WAVEFORM IN
CIRCUIT 83

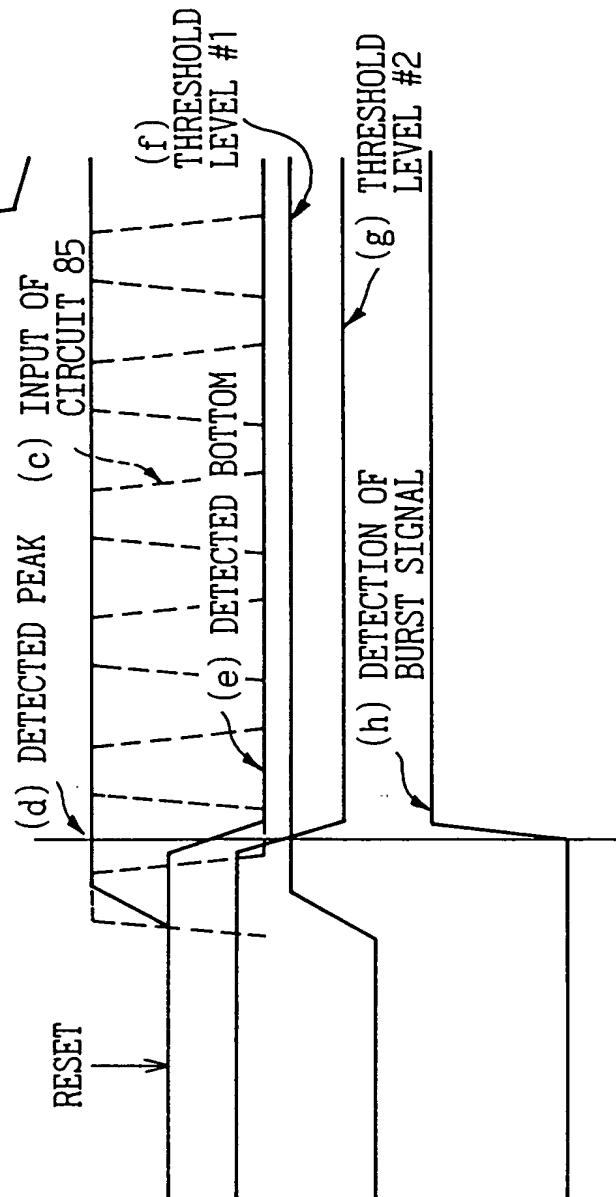


Fig. 9B WAVEFORM IN
CIRCUIT 85



Fig. 9C OUTPUT OF
COMP 85A

Fig.10A

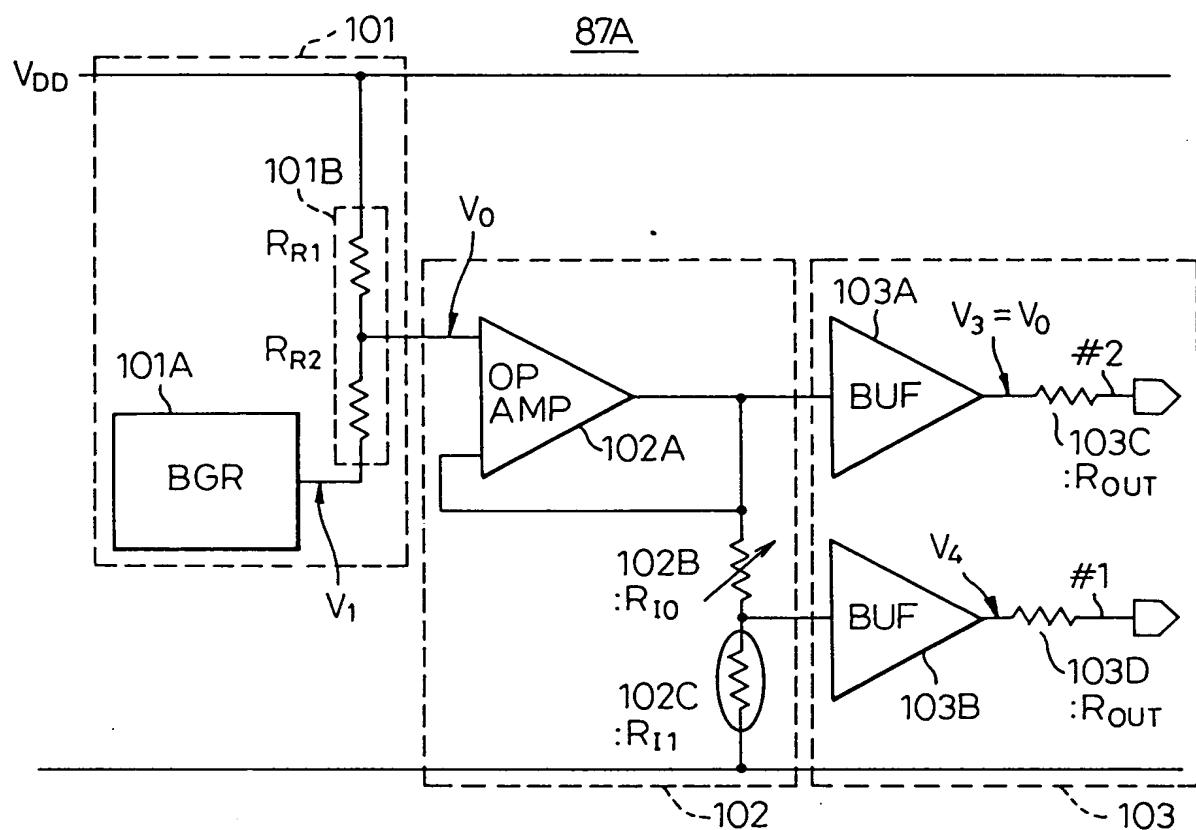


Fig.10B

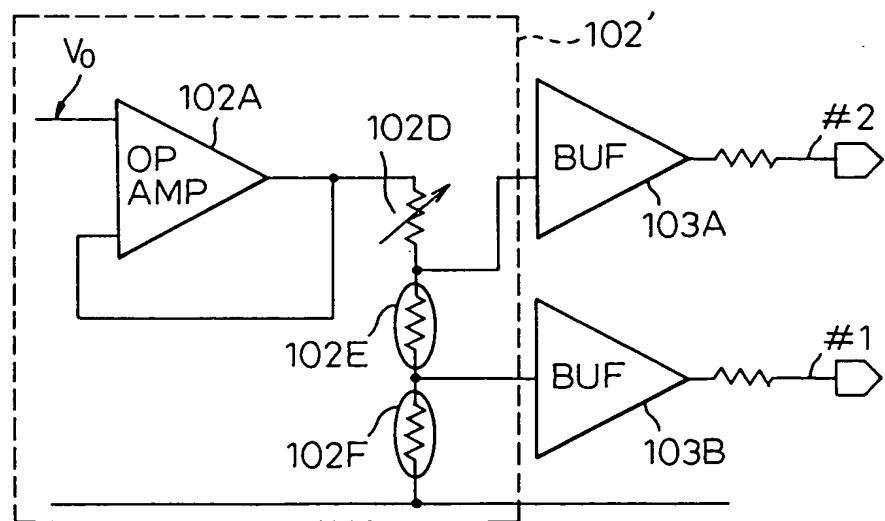
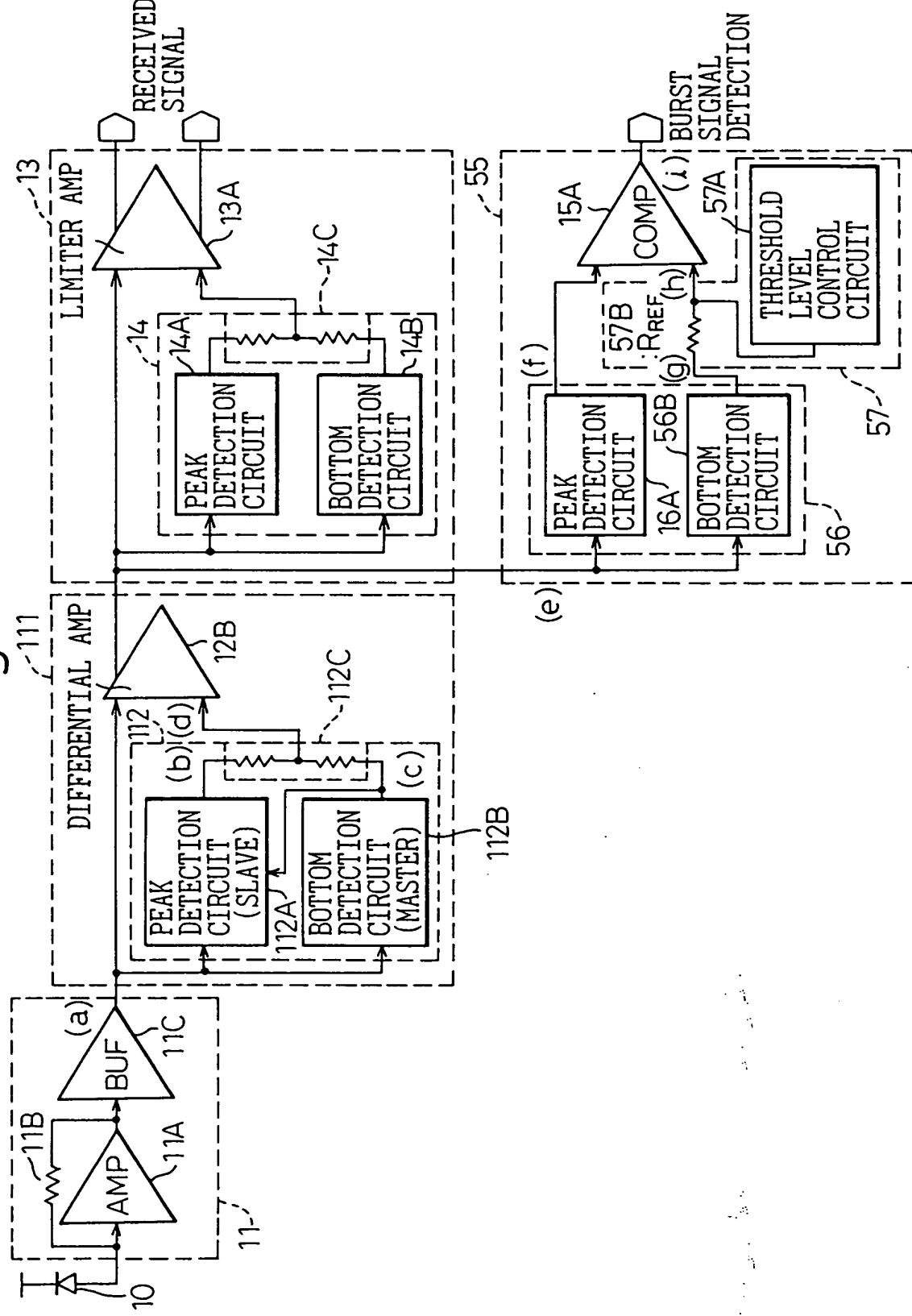


Fig.11

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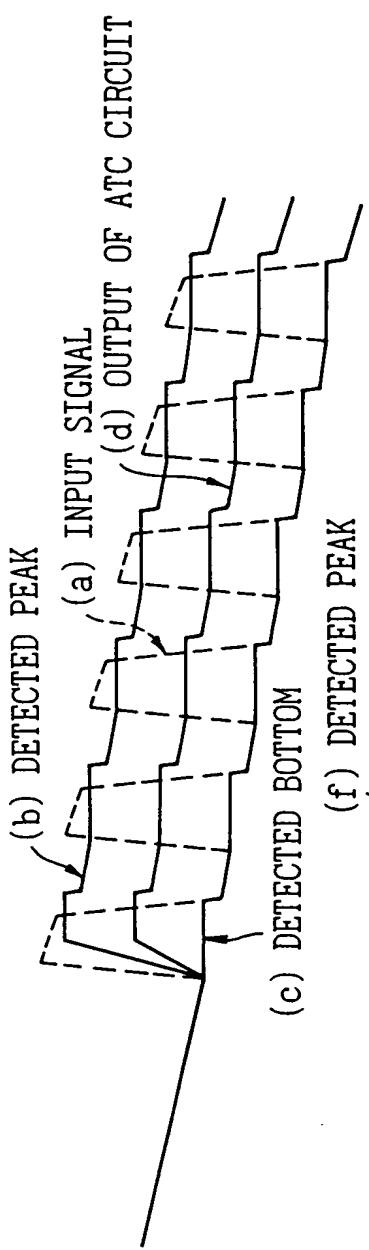


Fig.12A WAVEFORM IN CIRCUIT 111

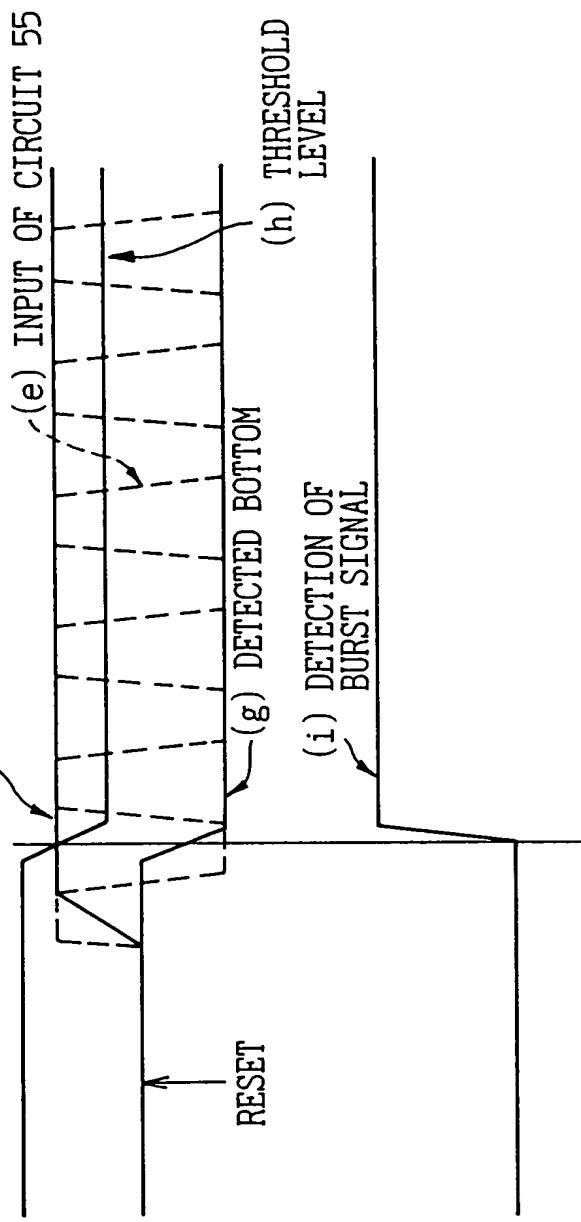


Fig.12B WAVEFORM IN CIRCUIT 55

Fig.12C OUTPUT OF COMP 15A

Fig.13

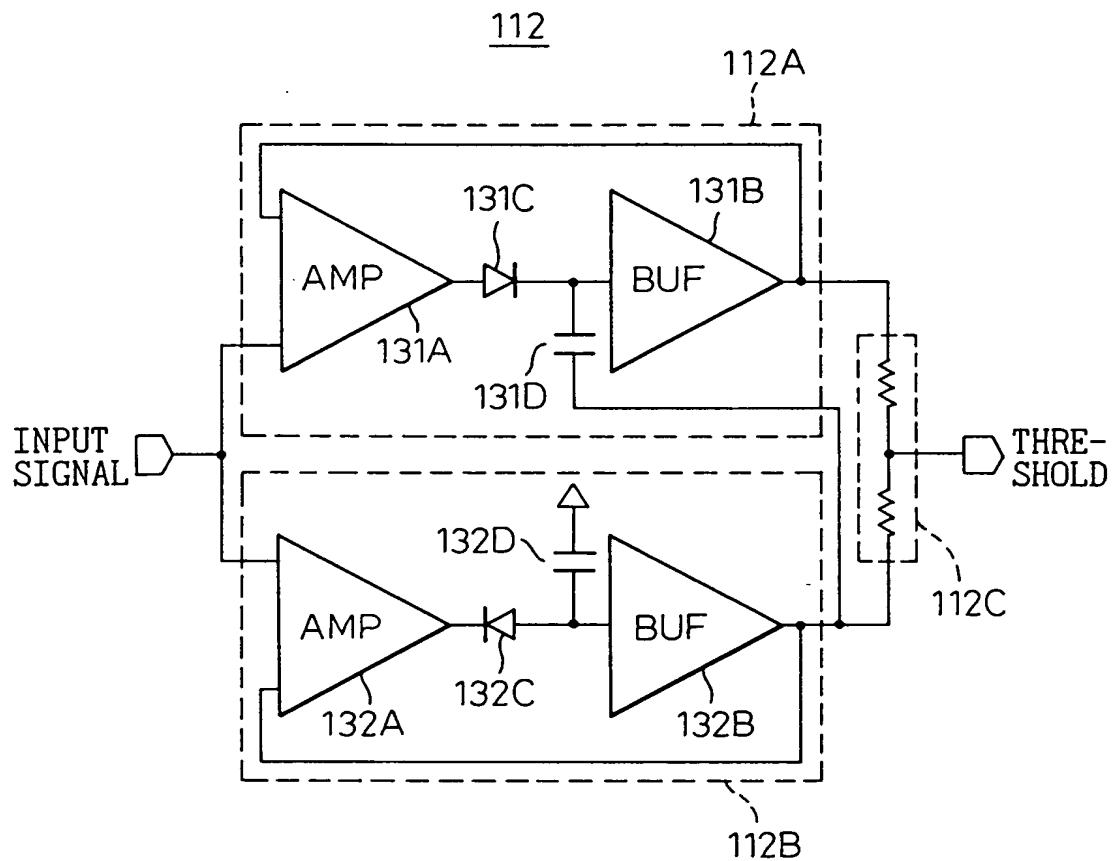


Fig. 14

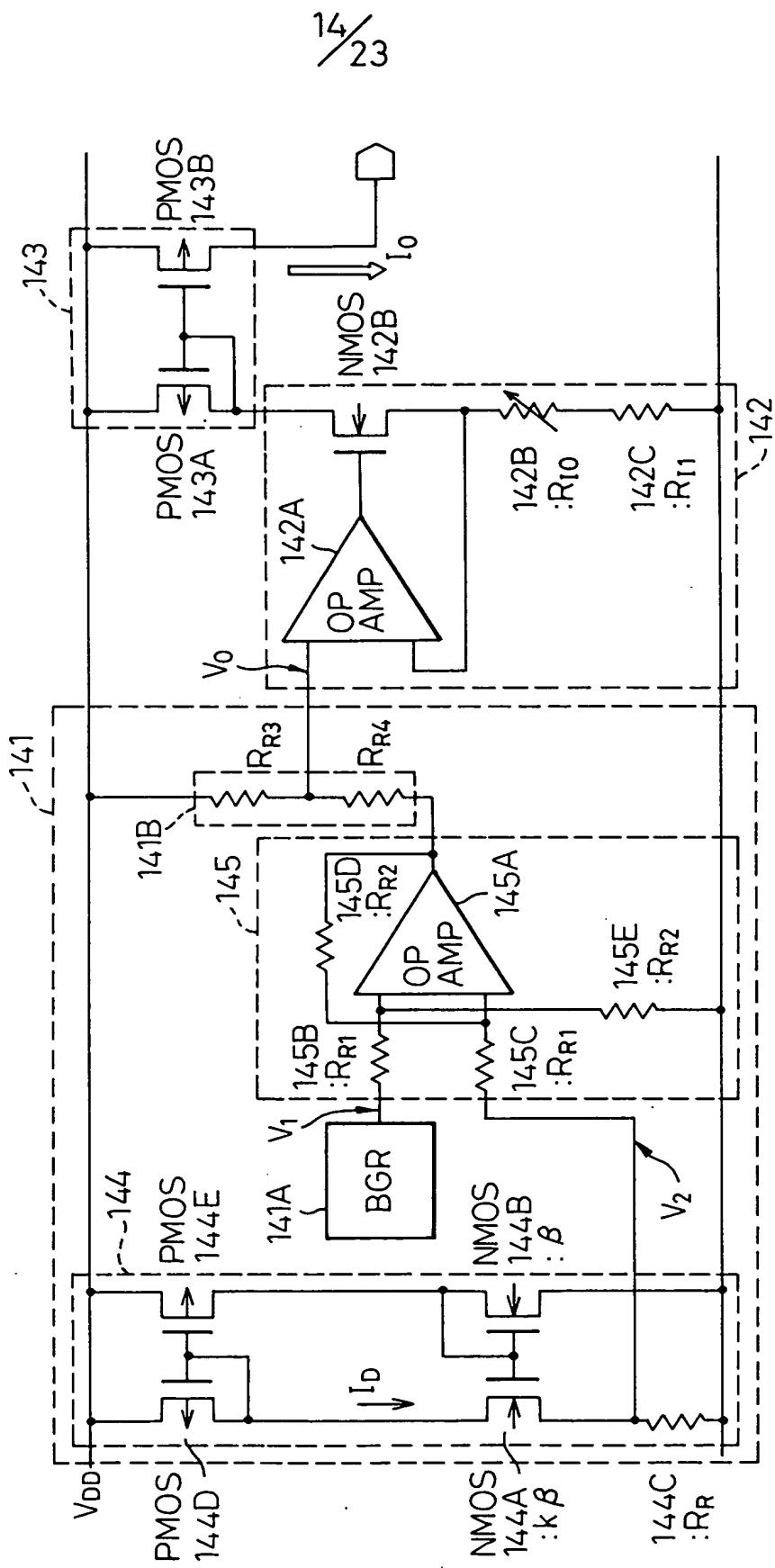
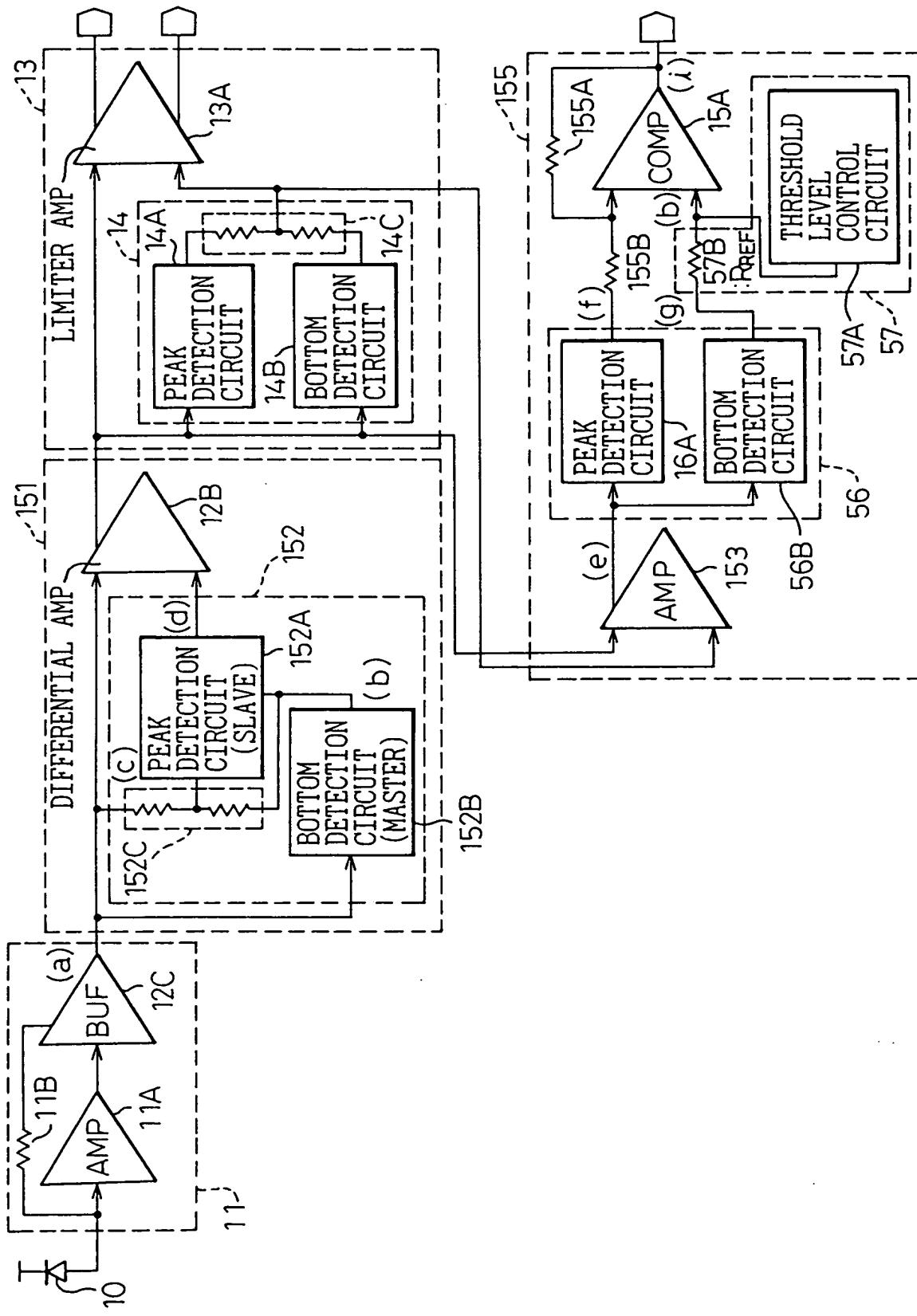


Fig. 15



(b) DIVIDED VOLTAGE SIGNAL

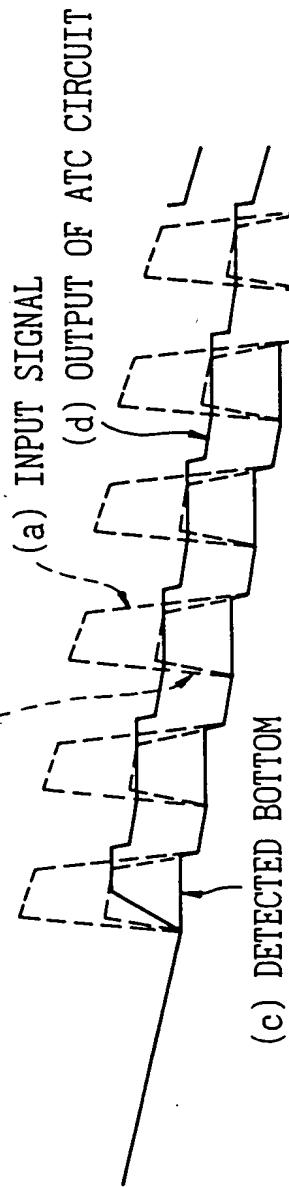


Fig.16A
WAVEFORM IN
CIRCUIT 151

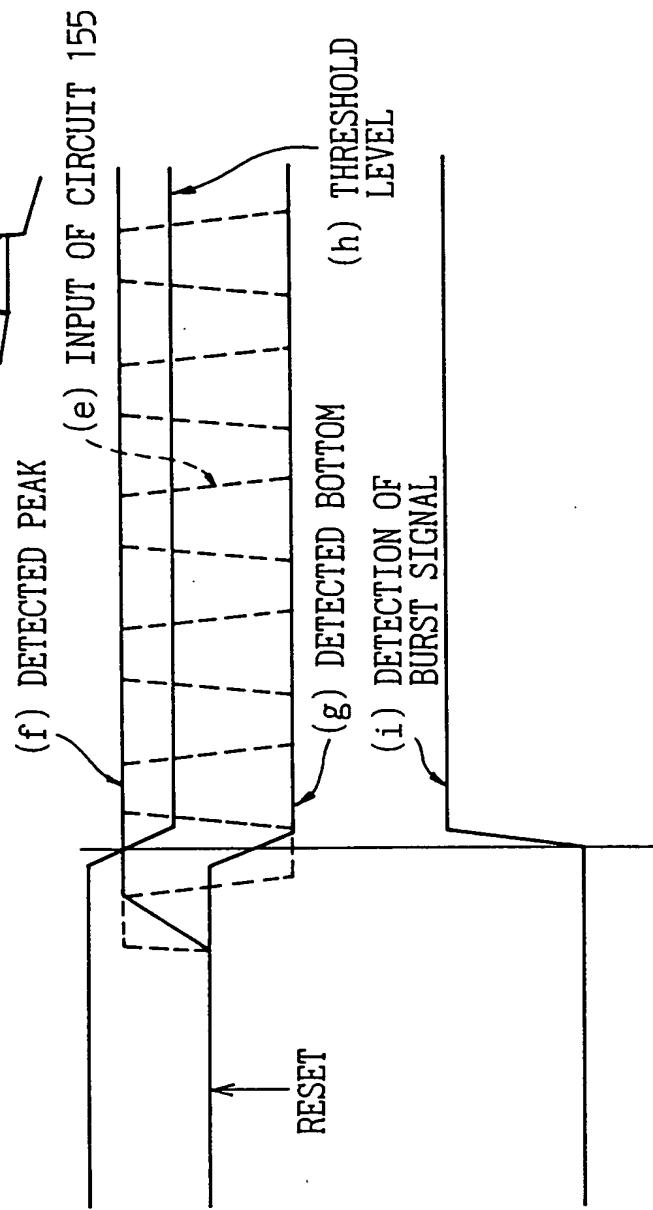


Fig.16B
WAVEFORM IN
CIRCUIT 155

Fig.16C
OUTPUT OF
COMP 15A

Fig.17

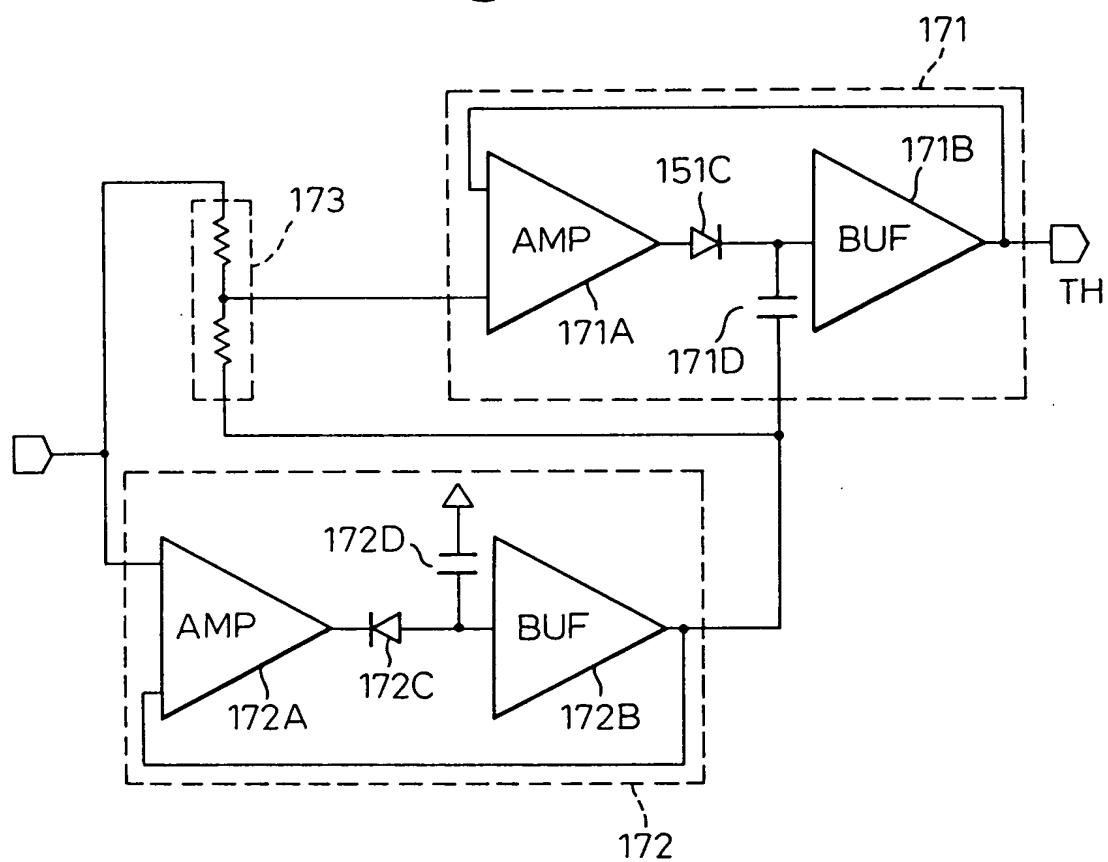


Fig. 18

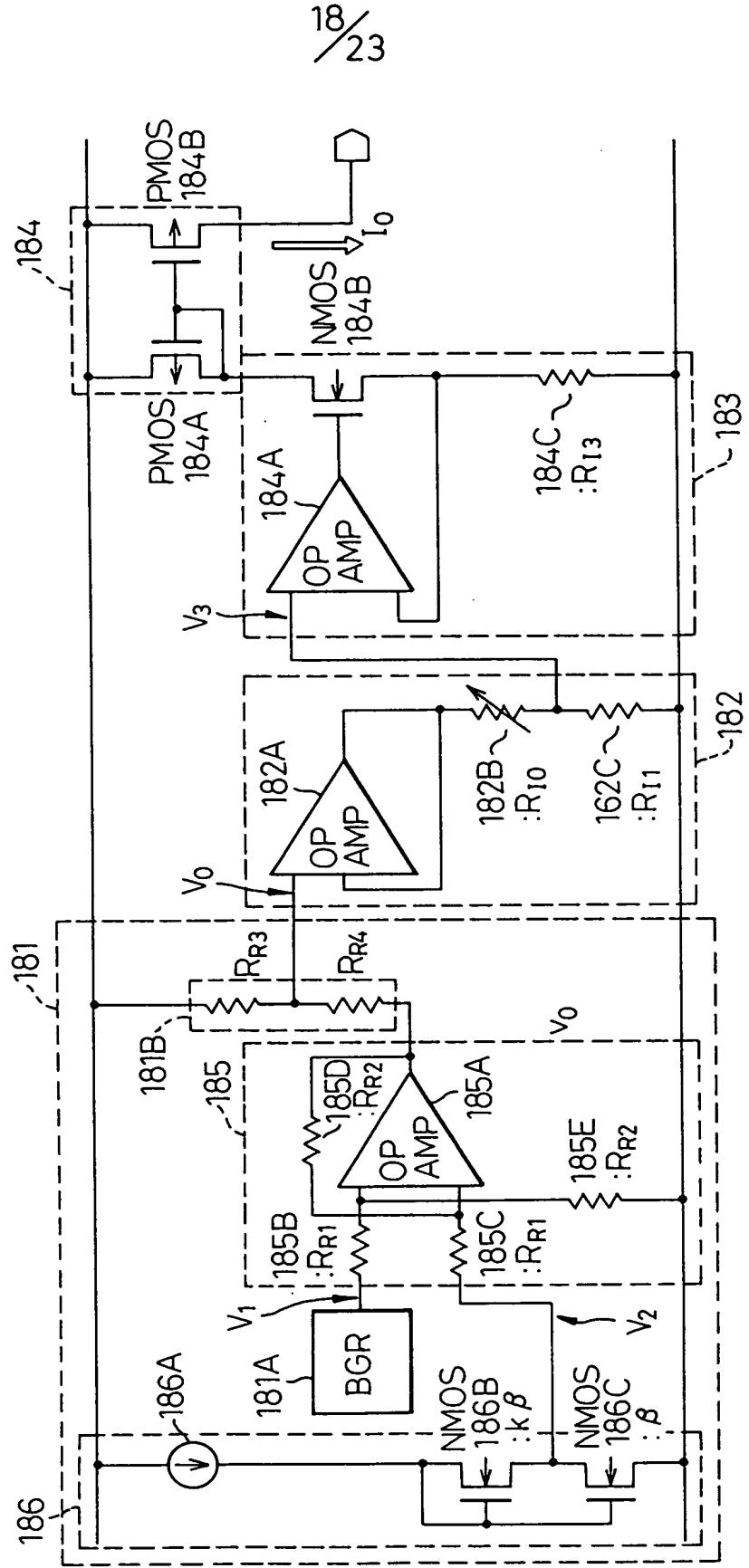
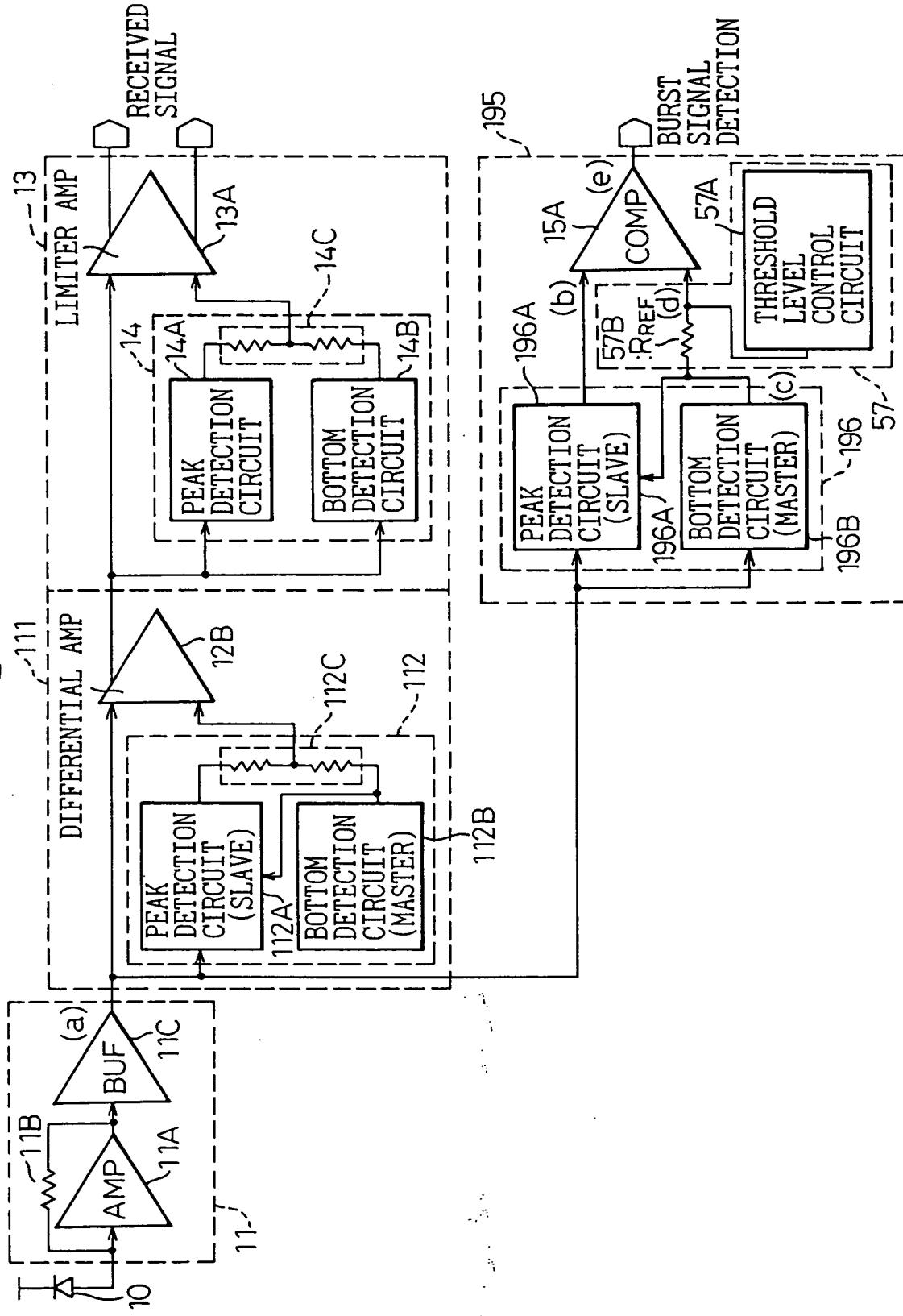


Fig. 19



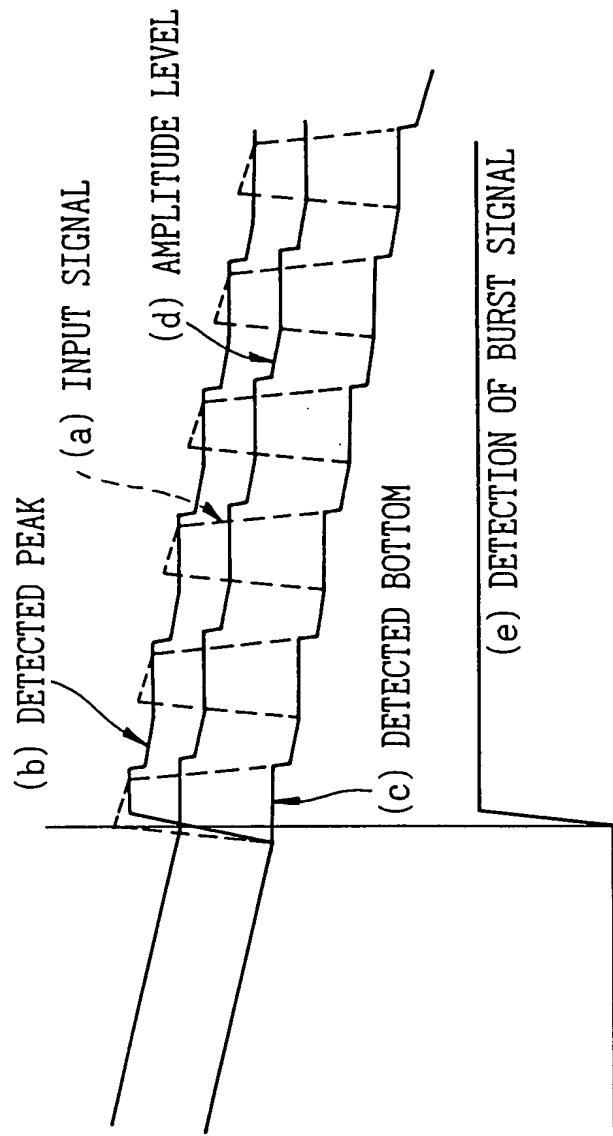
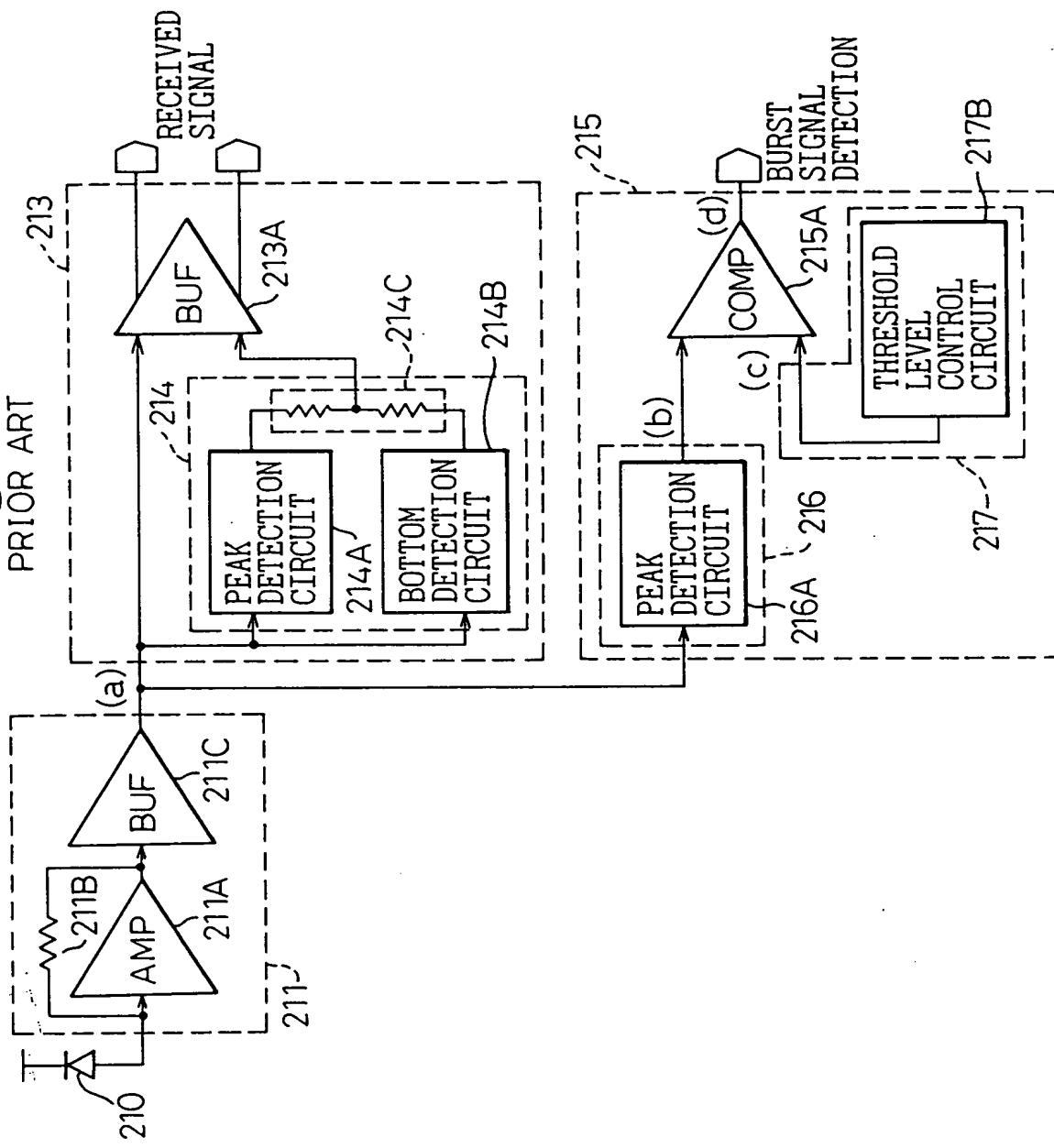


Fig.20A WAVEFORM IN CIRCUIT 195

Fig.20B OUTPUT OF COMP 15A

Fig. 21



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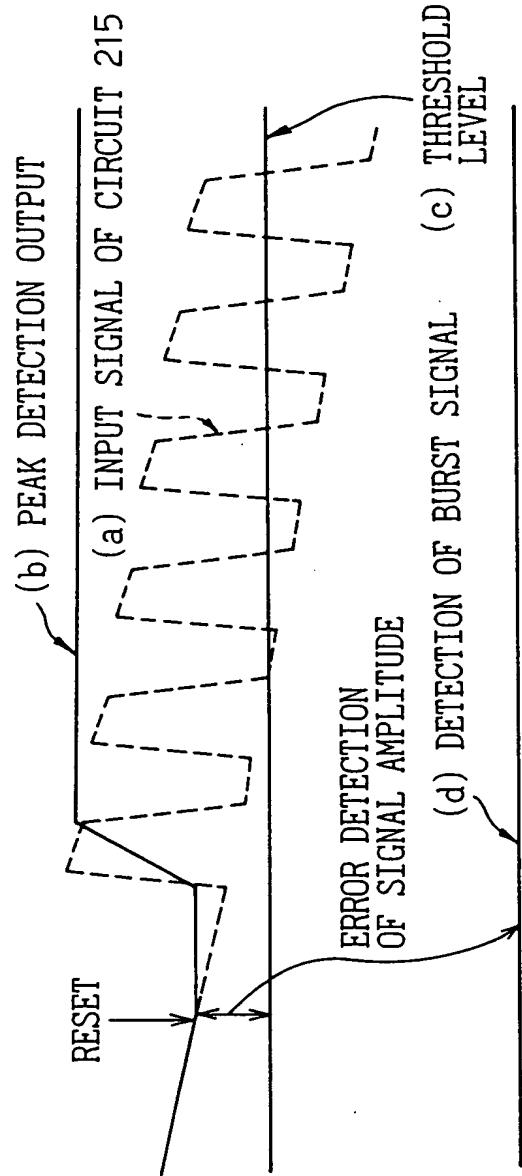


Fig.22A
WAVEFORM IN
CIRCUIT 215
PRIOR ART

Fig.22B
OUTPUT OF
COMP 215A
PRIOR ART

(c) THRESHOLD
LEVEL

(d) DETECTION OF BURST SIGNAL

ERROR DETECTION
OF SIGNAL AMPLITUDE

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Fig.23A

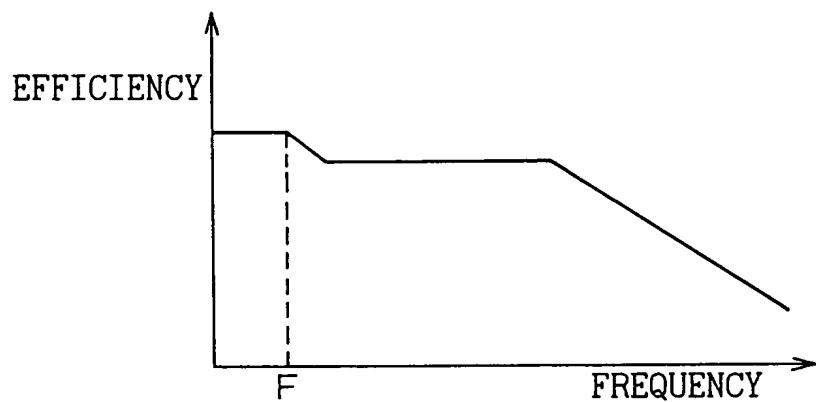


Fig.23B

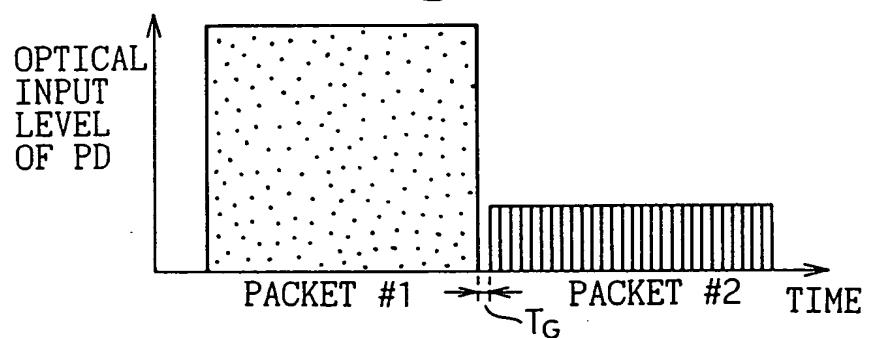


Fig.23C

